

**Amendments To The Specification**

Please replace paragraph in the Specification page 9, lines 2-8 with the following amended paragraph:

FIG. 4D illustrates a process of converting a falling pulse signal into a pulse signal having a predetermined width. In the same manner as in ~~FIG 3C~~ **FIG 4C**, the pulse conversion unit modulates the pulse width of the input pulse signal to output the pulse-width-modulated pulse signal. Also, the pulse-width-modulated pulse signals may be a low or high-level pulse having a predetermined pulse width.

Please replace paragraph in the Specification page 9, lines 12-19 with the following amended paragraph:

Next, in FIG. 3, the shared delay unit ~~302~~ **320** includes one delay unit 330, and this delay unit 330 receives a plurality of pulse signals PULSE0, PULSE1, PULSE2 PULSE3, and then outputs delay signal DELAY OUTPUT delayed for a predetermined time. Here, the shared delay unit 320 applies a plurality of pulse signals PULSE0, PULSE1, PULSE2 and PULSE3 to the delay signal ~~DELAT~~ **DELAY** OUTPUT outputted from the delay unit 330.